

<b>Notice of References Cited</b>	Application/Control No. 10/039,289		Applicant(s)/Patent Under Reexamination WOLRICH ET AL.	
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	H	US-5,893,162	04-1999	Lau et al.	711/153
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	V	Sivarama P Dandamudi, "multiprocessors", IEEE computer, March 1997, pp 82-89
	W	Maged M Michael ; "scalable lock-free dynamic memory allocation", PLDI,'04, ACM, June 2004, pp 1-12
	X	Sailesh Kumar et al. "A scalable, cache-based queue management subsystem for network processors", no date, pp 1-7

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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